

BITBUS INTERFACE FOR VME *with fifo buffers and signal isolation*



FEATURES

- Full support of the BITBUS specifications
- 8044 BITBUS Enhanced Micro Controller (BEM) at 12 MHz
- Self clocked & synchronous communication mode (up to 2.4 Mb/s)
- Signal isolation between the BITBUS interconnect and the controller board (for self clocked & synchronous communication modes)
- BITBUS interconnect available via two 9 pin female D type connectors / P2 connector / plastic fiber optic (type 5730 only)
- Two 1k deep FIFO buffers between 8044 and host interface
- 7 software selectable FIFO interrupt sources
- 16k EPROM & 32k RAM for local program(s)
- Auto start facility from local task(s) after power-on (EPROM only) or reset (RAM & EPROM)
- Auto configuration management
- Compatible with the VMEbus standard (Rev. C.1)

Type: VME-VEBB-5727: BITBUS VMEbus interface without plastic fiber optic.
VME-VEBB-5730: BITBUS VMEbus interface with plastic fiber optic.

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SPECIFICATIONS

GENERAL

- Self test at power-on and reset
- 16k EPROM (code space)
- 32k RAM (24k can be used as code space)
- Auto start facility from local task(s) after power-on (EPROM only) or reset (RAM & EPROM)
- Table in EPROM for auto configuration management
For more information, see the description about "Configuration Management"
- Communication speed :
 - self clocked mode 62.5 kb/s or 375 kb/s
 - synchronous mode 1 Mb/s and optional up to 2.4 Mb/s.
- Signal isolation between the BITBUS interconnect and the controller board (for self clocked & synchronous communication modes) up to 500V
- BITBUS interconnect available via:
 - 2 times 9 pin female D type connector
 - 2 times on the P2 connector
 - 2 times plastic fiber optic (type VME-VEBB-5730 only)

The double BITBUS connectors are for an easy connection of the BITBUS cable

- Compatible with the VMEbus standard (Rev. C.1)
- VME short supervisory and/or short non-privileged address-space
- A16 : D08 (0) DTB slave
- Interrupter - (IRQ1-IRQ7); programmable interrupt vector
- Two 1 k deep FIFO buffers to avoid host processor overhead
- 7 software selectable FIFO interrupt sources on:
 - Transmit FIFO not full
 - Transmit FIFO not almost full
 - Transmit FIFO empty
 - Receive FIFO not empty
 - Receive FIFO almost full
 - Receive FIFO full
 - Receive command
- Power supply requirements : +5 V (+/- 5 %), 1.5 A typ

MECHANICAL CHARACTERISTICS

- Board dimensions : 6 U high, 234 x 160 mm Eurocard
- BITBUS connector : 9 pin female D type.
plastic fiber, HP T-1521 & HP R-2521
(type VME-VEBB-5730 only)

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Other specifications on request.
Specifications are subject to change without notice.

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